

PATENT NUMBER

I.P.E. SCANNED PATENT DATE	PATENT DATE
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Michimasa Funabaz

BEST AVAILABLE COPY

Method of manufacturing a semiconductor integrated circuit device

PTO-204
12/89

GENERAL DISCLAIMER	DRAWINGS			CLASSES ALLOWED	
	Acoustic Equip.	Figs. Drawg.	Print Ftg.	Total Allowed	Other Classes Not O.C.
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